

**REMARKS**

In response to the election requirement in the Official Action dated December 10, 2008, Applicant elects Invention II, Embodiment 2, method of manufacturing a thin film integrated circuit, with a connection region, Figures 8A-B, 9A-13C, and 21B. Claims 2, 3, 5-7, 9, 10, 12, 13, 15, 16, 18, 19, 21, 22, 24, 25, 27, 28, 30, 31, 33, 34, 36, 37, 39, 40, 42, 43, 45, and 46 read on the elected species, and are subject to examination at this time.

Claims 1, 4, 8, 11, 14, 17, 20, 23, 26, 29, 32, 35, 38, 41, and 44 have been canceled.

Examination on the merits is respectfully requested.

Respectfully submitted,

  
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